Optimized DSP Software • Independent DSP Analysis

Smart Processor Picks for Consumer Audio/Video Applications

(Workshop 270)

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Outline

- Motivation and scope
- Application requirements
- Challenges
- Processor architecture options
- Selection methodology
- Conclusions

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Motivation

- Technology creates new opportunities, e.g.,
  - Broadband internet enables video on demand
  - Product convergence: cellphone+camera, digital still+video camera
- "Right" processor key to product success
  - Supports, enables desired product features
  - Heavily influences product cost, power consumption, performance (end user experience)
  - Can simplify development effort and cost
- Range of processor options is large, dynamic, and growing, making selection difficult

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Scope

- Processor selection for consumer media products with varying features:
  - Application a mix of audio, video, or still image
    - MP3 players, voice recorders, cell phones
    - Still or video cameras, set-top boxes
  - Using streaming or stored content
  - Battery or line powered, portable or fixed
  - Cost constrained
  - Input/output quality varies by application
    - E.g., lower quality audio for voice recorder, high quality audio for MP3 or DTS playback

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Player/DRM Requirements

- Manages other application sub-modules (e.g., codecs), provides user interface
- Processing requirements: 1's–10's MIPS
- Good tools are critical
- Processor features that benefit compilers are useful, e.g.,
  - Orthogonal instruction set
  - Large, linear address spaces
  - Flexible data type support
- I/O bandwidth requirements depend on:
  - Application features, peripheral mix
  - Software architecture

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Audio and Video Codec Requirements

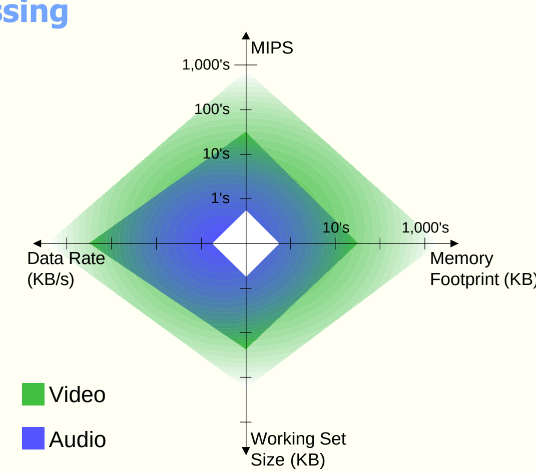
Including Post-Processing

Audio: less demanding

- "MP3," MPEG-4 AAC, DTS, RA8
- Sample rate conversion, equalization
- Higher precision (≥16 bits)
- Low throughput

Video: more demanding

- MPEG-2, MPEG-4 AVC, WMV9, DivX ...
- Alpha blending, scaling
- Lower precision (≤16 bits)
- High throughput



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I/O Requirements

- "Connected" products must support multiple I/O interface standards
  - Basic in-system serial & parallel (CCD, I^2S, SPI, "host port")
  - Storage ports (glueless SDRAM, ATA, flash)
  - External connectivity (Ethernet, USB, 1394, wireless)
- Support for high transfer rates
  - Video data rates range from 100's—1000's KB/s
- Autonomous, intelligent I/O
  - E.g., programmable communications co-processors reduce load on core processor
  - Support for IP reuse to ease development

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Development Effort and Cost

- Development effort affected by many factors
  - Programming model complexity
    - More powerful processor → more complex model
    - More complex model → increased development effort
    - Don't overlook complexity of intelligent I/O
  - Availability of off-the-shelf software components
    - Codecs
    - OSs
    - Device drivers
  - Reference designs
  - Quality of tools
    - Maturity, capability of development tools
    - Support for I/O in debug
- The right choice of processor can reduce development effort and cost

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Processor Selection Challenges

The fundamental problem:

- Many processors and types of processors to choose from
- Complex processors, applications
- Multiple standards to be supported
- Many important selection criteria to consider
- Unpredictable dynamism in processor options, application requirements
- Poor information, complex analysis
- Limited time and resources for selection

The wrong choice can be fatal for a product development effort

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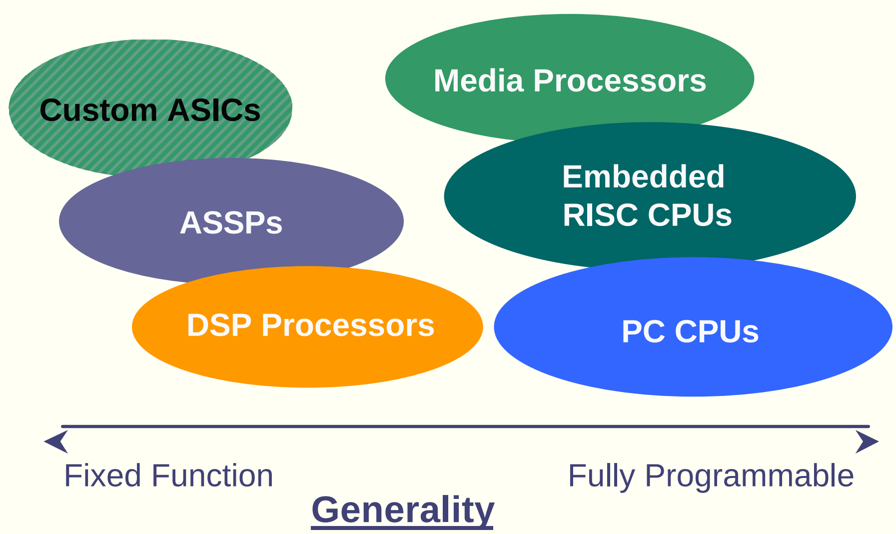
Processor Categories



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ASICs

Strengths and Weaknesses

- ⬆ Offers the ultimate in tailored hardware
  - ⬆ Speed, energy efficiency, cost/performance …
  - ⬆ Integration to match the product requirements
  - ⬇ Design usually inflexible
- ⬇ Large development costs and risks vs. off-the-shelf hardware; NRE $ increasing
  - ⬇ Iteration is costly and time consuming
  - ⬇ Lengthy development cycles
- ⬇ Hardware/software integration and whole-chip testing are particularly challenging
  - ⬇ Hardware/software partitioning typically must be done early
- ⬇ Complex, costly, unreliable tools
- ⬆ Vast architectural options

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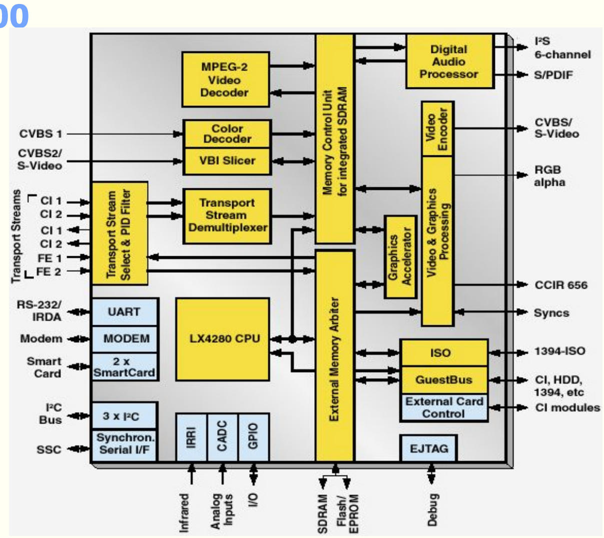
Example ASSP

Micronas MDE9500

- High-integration digital TV receiver
- Analog decode, DVB decryption, decode
- On-chip MPEG-2 video decoder (D1, 30 fps)
- Interfaces to other chips for, e.g., PVR functionality
- Customizable via software
  - MIPS-compatible CPU
  - Supports DVB-MHB
  - Supports Java
- Price $15-$30, qty 10k



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ASSPs
Strengths and Weaknesses

- Often very well matched to the application
  - SoCs with extensive integration
  - Architecture tuned for the application
  - Can yield excellent performance, cost, energy efficiency
- Ease of use
  - Reduce system development costs
  - Reduce required implementation expertise
- Often inflexible
- Limited differentiation opportunities for system designer
- Usually single-source
- Roadmap often unclear

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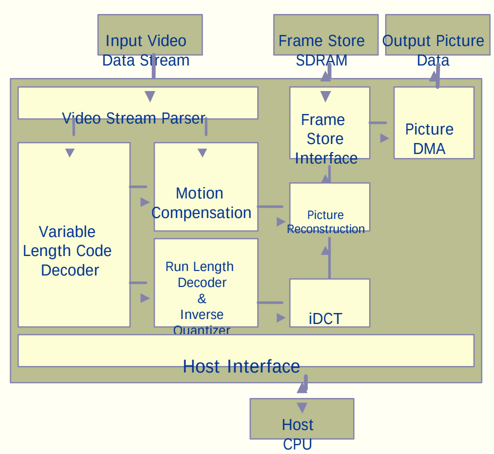
Example FPGA
Altera Stratix EP1S20

- Diverse on-chip hardware:
  - ~1.6 Mbits RAM
  - 18,460 logic elements
  - 80 embedded multipliers
  - 10 DSP blocks
  - 6 PLLs, 586 I/O pins
- Specialized high-speed I/O support
  - HyperTransport, PCI-X, SDRAM
- MPEG-2 HD decode
  - 4:2:0 at 108 MHz
  - 4:2:2 at 133 MHz
- Price $66 in qty 10k
  - 60-70% cost reduction with "Hardcopy"



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FPGAs

Strengths and Weaknesses

- ⬆ Massive performance gains on some algorithms
  - ⬆ ~50X throughput, cost/performance advantage over DSP/GPP processors in some applications
- ⬆ Architectural flexibility can yield efficiency
  - ⬆ Adjust data widths throughout algorithm
  - ⬆ Parallelism where you need it; distributed storage
- ⬆ Re-use hardware for diverse tasks
- ⬇ Slow time-to-market compared to, e.g., DSP/GPP
  - ⬇ Cumbersome design flow is unfamiliar to most signal-processing engineers
  - ⬇ Proprietary architectures
- ⬆ Suitability for single-channel, low-power, cost-sensitive signal-processing applications unclear

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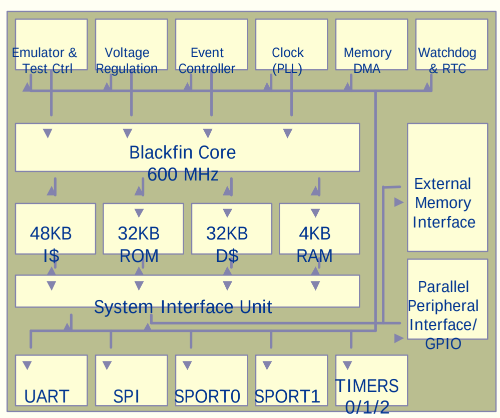
Example DSP Processor

Analog Devices ADSP-BF533

- Enhanced 600 MHz, 16-bit fixed-point DSP with dual-MAC units
  - 750 MHz also available
- ADSP-BF53x BDTImark2000™ score: 3,360
- Integrated peripherals target media apps (e.g., CCIR-656 port, I^2S ports)
- Good 3rd party software component support
- MPEG-4 decode, simple profile, CIF: 168 MHz
- Price $16, qty 10k



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DSP Processors

Strengths and Weaknesses

- ↑ Performance, efficiency on media applications strong compared to other off-the-shelf processors
- ↓ But not as strong as customized solutions, and may not be adequate for demanding tasks
- ↑ Media-oriented development tools, infrastructure
- ↓ Tools not as sophisticated as those available for general-purpose processors
  - ↓ Often, poor compiler quality
- ↑ Mature technology
- ↑ Third-party audio/video application software available
  - ↓ Support for non-DSP software not as strong as, e.g., RISC
- ↑ Relatively low development cost, risk

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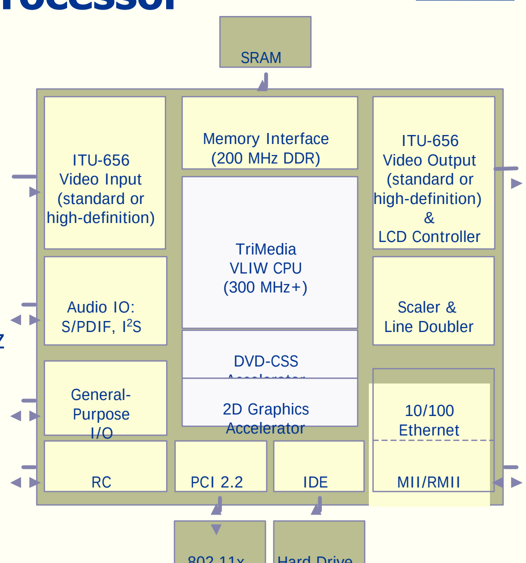
Example Media Processor

Philips PNX1500

- General-purpose 300 MHz five-way VLIW
- On-chip L1 data, instruction caches, and L2 data cache
- Media-specific interfaces, co-processors, instructions
- C/C++ programming model
- MPEG-4 decode (simple profile, CIF, 30 fps): 45 MHz
- MPEG-4 D1 video + audio encoding in real time
- Price <$20, qty >100k



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Media Processors

Strengths and Weaknesses

- ⬆ Higher performance than most DSPs, GPPs
  - ⬆ VLIW, huge register sets, wide SIMD typical
  - ⬆ High performance peripherals, co-processors
- ⬇ Very complex programming models
- ⬆ Better support for media processing in development tools, infrastructure, compared to GPPs
- ⬇ Application performance compiler-dependent
  - ⬇ Compilers can be poor quality
- ➡ Maturing technology—but roadmaps unclear
  - ⬇ 3rd party support weaker than other processor types
- ⬆ Development cost, risk, lower than ASIC, FPGA

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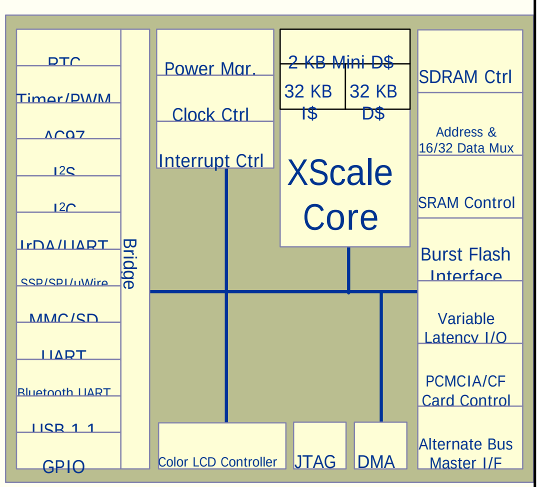
Example Embedded RISC CPU

Intel PXA255

- 400 MHz, 32-bit RISC with modest DSP extensions
- BDTIMark2000™ score: 930
- MPEG-4 decode (simple profile, CIF @ 30 fps) 200 MHz
- 16-bit SIMD, 32-bit data types benefit media apps
- Predicated instruction execution good for control
- Good development tool support, optimized DSP software available (e.g., Intel IPP), good OS options
- Price $35, qty 10k



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Embedded RISC CPUs

Strengths and Weaknesses

- Can have adequate performance on media applications
  - Often less efficient that DSPs and media processors
- Dynamic features complicate programming
  - Complicates optimization & ensuring real-time
- Sometimes, convoluted programming model
- 32-bit GPPs better targets for non-media tasks
  - E.g., TCP/IP network stacks
- Multi-vendor architectures more common
- Good tools, but generally weak on support for media application development
- Very good third-party OS, software component support
- Compatibility more common
- High integration parts increasingly common

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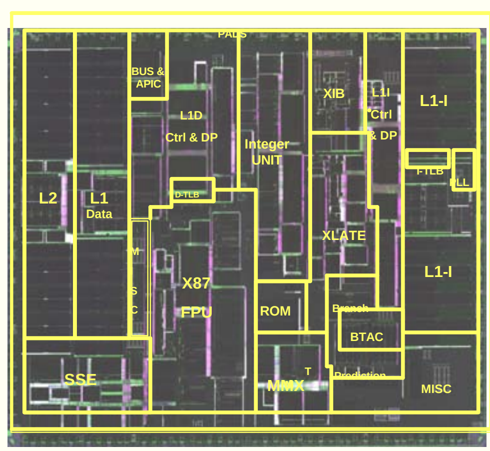
Example PC CPU

VIA Technologies C3

- 1 GHz x86 compatible
- Moderate power consumption, cost
- SSE support for media applications, supports fixed-, floating-point types
- Access to massive x86 3rd-party software, tools base
- Familiar to software, hardware developers
- MPEG-4 decode (D1, 30 fps) using 35% of CPU, when using VIA CN400 chipset
- CPU: $70, chipset: $23 (qty 10k)



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PC CPUs (GPPs)

Strengths and Weaknesses

- ⬆ High-performance GPPs (e.g., Intel Celeron, VIA C3) can implement complex media tasks
  - ⬆ May be as fast or faster than DSPs…
  - ⬇ … but cost & power consumption typically higher
- ⬇ Dynamic features complicate optimization, real-time design
- ⬆ Many options for OS, 3rd party application software
- ⬆ Development tools mature, powerful
  - ⬇ But typically lack features useful for media application development

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Processor Selection Methodology

Use a hierarchical approach to make the problem manageable:

- Determine selection criteria
- Prioritize or assign weights to selection criteria
- Use critical criteria to eliminate obviously unsuitable choices
  - Begin with classes of processors
- Evaluate and rank candidates
- Weigh trade-offs among non-critical criteria
- Iterate as necessary
  - Refine criteria and analysis of candidates

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Processor Selection Criteria

Signal-Processing-Centric Concerns

- Performance on relevant audio/video tasks
  - Speed
  - Memory bandwidth: on-chip, off-chip
  - Execution-time predictability
    - Dynamic features confound determinism
  - Energy consumption
  - Fixed-point vs. floating-point
    - Floating-point less important for video
  - Data word size(s)
- Memory usage

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Processor Selection Criteria
Signal-Processing-Centric Concerns

- On-chip integration
  - Memory, peripherals, I/O interfaces, coprocessors
- Development effort, risk
  - Media-oriented tools, infrastructure
  - Programming model complexity
  - Application software components
  - Tools, support (vendor, 3rd party)
  - Features useful for integration, real-time testing
    - E.g., on-chip debug support
  - Accurate cycle-count and memory profiling
  - Visibility into cache, pipeline

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Processor Selection Criteria
General Concerns

- Cost
- Packaging options
- Roadmap
  - Availability; reliability of supply
    - Multi-vendor architectures a plus
  - New spins, new architectures, compatibility
  - Core version available?
- Special requirements
  - Variable-voltage operation

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Assessing Performance

- Use results from relevant application modules
  - More accurate than kernel benchmark mapping—if available
  - Use caution! The data may be misleading or incomplete.
- Use kernel benchmarks & application profile
  - Useful when application data isn't available
  - Use kernel benchmark results to predict application module performance
- Use care with either approach
  - Hazards include data types, multitasking effects ...

Assessing Performance, continued

- Core CPU performance isn't enough
  - Must also consider memory sizes and bandwidths
  - I/O bandwidths and overheads: data movement can be very costly
- Impact of software partitioning in multi-processor systems
  - Must refine software architecture to predict performance
- Dynamic features complicate performance prediction
- Assessing energy efficiency can be very difficult

Development Considerations

- Language support
  - Quality of C compiler; availability of C++ compiler
  - Support for assembly language optimization
- Software availability
  - Media processing components
  - Player, device drivers, operating system
- Hardware/software reference designs
- Debug/development benefit from tools with:
  - Peripheral and multi-processor simulation
  - Non-intrusive, real-time debug
- Compatibility, developer familiarity

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Availability and Roadmap

- Risk
  - Is the chip available in volume today?
  - Are there second sources of the chip or compatible chips?
  - What does the errata list look like?
- Roadmap
  - What is the vendor's commitment to evolving the chip? E.g., improved integration, reduce cost
  - What is the vendor's roadmap for next-generation chips? Compatibility?
  - What is your confidence that the vendor will execute on its roadmap?

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Conclusions

- Choosing a processor for a consumer media product is easy
- Choosing the best processor for your particular product is hard
  - Vast range of options
  - Many complex, competing criteria to consider
  - Poor information
  - Limited time and resources

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Conclusions, cont.

- Use a hierarchical approach
  - Develop a well-defined hierarchy of product requirements
  - Start with the critical criteria and iteratively narrow the field
  - Expect to make trade-offs
- Assessing performance is a challenge
  - Resource-hungry algorithms, cost-constrained processors, many variables
- Development-related considerations are key
- Appropriate integration is essential to low system cost

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Trends: Processors

- Consumer media applications are becoming a major focus of processor vendors
  - Expect more competitors, more options
- Technology, competition pushes performance up; price, power consumption down
  - Enabling new types of products, new levels of functionality
  - But not all processors are well matched to media processing workloads
- Increasing architectural complexity
  - Many heterogeneous multiprocessors
- Integration increasing
- Development infrastructure is a key differentiator

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Trends: Development

- Products are becoming more complex
  - MP3 player vs. multimedia cell phone
- Processors are becoming more complex
- Algorithms are becoming more demanding
  - Nobody knows which ones will dominate
- Optimization continues to be essential
- Huge processor-to-processor differences in development infrastructure
  - Support for media applications
  - Off-the-shelf, optimized software components increasingly important

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For More Information…

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Free Information
- BDTImark2000™ scores
- DSP Insider newsletter
- Pocket Guide to Processors for DSP

White papers on processor architectures and benchmarking

Article reprints on DSP-oriented processors and applications
- EE Times
- IEEE Spectrum
- IEEE Computer and others

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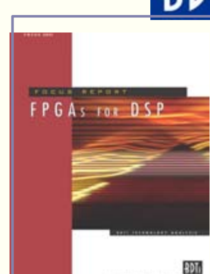

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