Speed Scores for Fixed-Point Packaged Processors (Higher is Better) BDTImark2000™ and BDTIsimMark2000™ (Single-Core Scores)

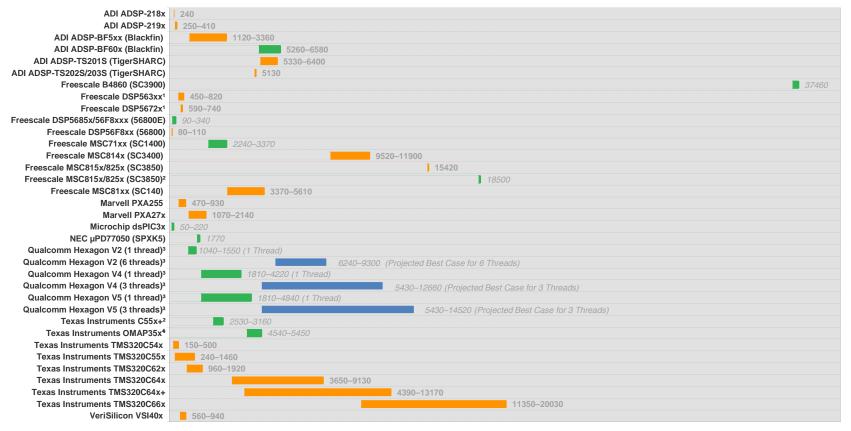


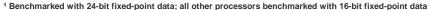
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See page 2 for details.





- ² Not available to the general market
- ³ Lower range of score is official single-thread BDTIsimMark2000, higher score is projecteded best case score using the maximum number of available threads (not an offical BDTIsimMark2000 score).
- 4 Metrics are for ARM Cortex-A8 core only ('C64x+ DSP is also available in some family members)

BDTIsimMark2000™ scores may be based on projected clock speeds.

For more information, see www.BDTI.com/Services/Benchmarks



Speed Scores for Fixed-Point Packaged Processors (Higher is Better)

BDTImark2000™ and BDTIsimMark2000™ (Single-Core Scores)

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Processor Family	Clock Rate (min-max)	BDTImark2000™, <i>BDTIsimMark2000</i> ™ (min-max)
ADI ADSP-218x	80 MHz	240
ADI ADSP-219x	100–160 MHz	250–410
ADI ADSP-BF5xx (Blackfin)	200–600 MHz	1120–3360
ADI ADSP-BF60x (Blackfin)	400–500 MHz	5260–6580
ADI ADSP-TS201S (TigerSHARC)	500–600 MHz	5330-6400
ADI ADSP-TS202S/203S (TigerSHARC)	500 MHz	5130
Freescale B4860 (SC3900)	1200 MHz	37460
Freescale DSP563xx1	150–275 MHz	450–820
Freescale DSP5672x1	200–250 MHz	590–740
Freescale DSP5685x/56F8xxx (56800E)	32-120 MHz	90–340
Freescale DSP56F8xx (56800)	60-80 MHz	80–110
Freescale MSC71xx (SC1400)	200–300 MHz	2240–3370
Freescale MSC814x (SC3400)	800-1000 MHz	9520-11900
Freescale MSC815x/825x (SC3850)	1000 MHz	15420
Freescale MSC815x/825x (SC3850) ²	1200 MHz	18500
Freescale MSC81xx (SC140)	300-500 MHz	3370–5610
Marvell PXA255	200-400 MHz	470–930
Marvell PXA27x	312-624 MHz	1070–2140
Microchip dsPIC3x	16–70 MHz	50–220
NEC μPD77050 (SPXK5)	250 MHz	1770
Qualcomm Hexagon V2 (1 thread) ³	67-100 MHz (per thread)	1040–1550 (1 Thread)
Qualcomm Hexagon V2 (6 threads) ³	67-100 MHz (per thread)	6240–9300 (Projected Best Case for 6 Threads)
Qualcomm Hexagon V4 (1 thread) ³	100-233 MHz (per thread)	1810–4220 (1 Thread)
Qualcomm Hexagon V4 (3 threads) ³	100-233 MHz (per thread)	5430–12660 (Projected Best Case for 3 Threads)
Qualcomm Hexagon V5 (1 thread) ³	100-267 MHz (per thread)	1810–4840 (1 Thread)
Qualcomm Hexagon V5 (3 threads) ³	100-267 MHz (per thread)	5430–14520 (Projected Best Case for 3 Threads)
Texas Instruments C55x+2	400–500 MHz	2530–3160
Texas Instruments OMAP35x4	600-720 MHz	4540–5450
Texas Instruments TMS320C54x	50-160 MHz	150–500
Texas Instruments TMS320C55x	50-300 MHz	240–1460
Texas Instruments TMS320C62x	150–300 MHz	960–1920
Texas Instruments TMS320C64x	400–1000 MHz	3650-9130
Texas Instruments TMS320C64x+	400–1200 MHz	4390–13170
Texas Instruments TMS320C66x	850-1500 MHz	11350–20030
VeriSilicon VSI40x	120–200 MHz	560–940

¹ Benchmarked with 24-bit fixed-point data; all other processors benchmarked with 16-bit fixed-point data

BDTImark2000™, BDTIsimMark2000™: The BDTImark2000™ and BDTIsimMark2000™ provide a summary measure of signal processing speed. BDTIsimMark2000™ scores may be based on projected clock speeds. For more info and scores see www.BDTI.com/Services/Benchmarks

² Not available to the general market

³ Lower range of score is official single-thread BDTIsimMark2000, higher score is projecteded best case score using the maximum number of available threads (not an offical BDTIsimMark2000 score).

⁴ Metrics are for ARM Cortex-A8 core only ('C64x+ DSP is also available in some family members)