

## Speed Scores for Fixed-Point Licensable Cores (65 nm)

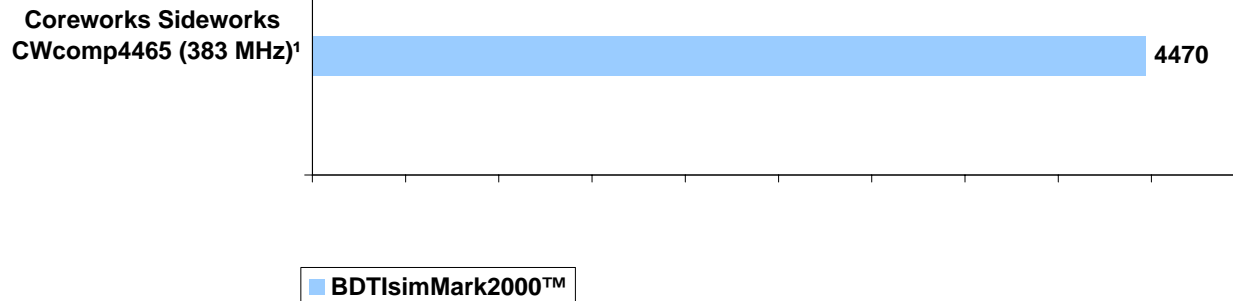
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Contact BDTI for authorization to publish scores.



Are you a processor vendor who could benefit from having BDTI DSP Kernel Benchmarks™ results for your processor? For more information, [click here](#).



Need more detailed benchmark results for these or other processors? Detailed results for each of the 12 BDTI DSP Kernel Benchmarks™, including cycle counts and execution times, are available in a variety of [BDTI Analysis Reports](#). For benchmark results for processors not shown here, [contact BDTI](#).

All processors benchmarked with 16-bit fixed-point data. All cores include at least 16 KB on-core memory and use worst-case clock speeds for the TSMC CLN65GP process and the Artisan Advantage core cell library. Vendors can choose different speed/area/power trade-offs; to understand the trade-offs, please view all BDTI metrics for each core. BDTIsimMark2000™ scores may be based on projected clock speeds. For information, see [www.BDTI.com/benchmarks.html](http://www.BDTI.com/benchmarks.html)

¹Coreworks scores include both a customized SideWorks DSP engine and the FireWorks 32-bit RISC processor. The SideWorks core used to implement the BDTI DSP Kernel Benchmarks includes four 16-bit multiplier units, six 32-bit ALUs, five shift units, six data multiplexing units, two data de-multiplexing units, two bit-reverse units, a bit unpack unit, and 6K bytes of memory. Different versions of the SideWorks core will yield different performance, power consumption, and die size figures than those reported here.