

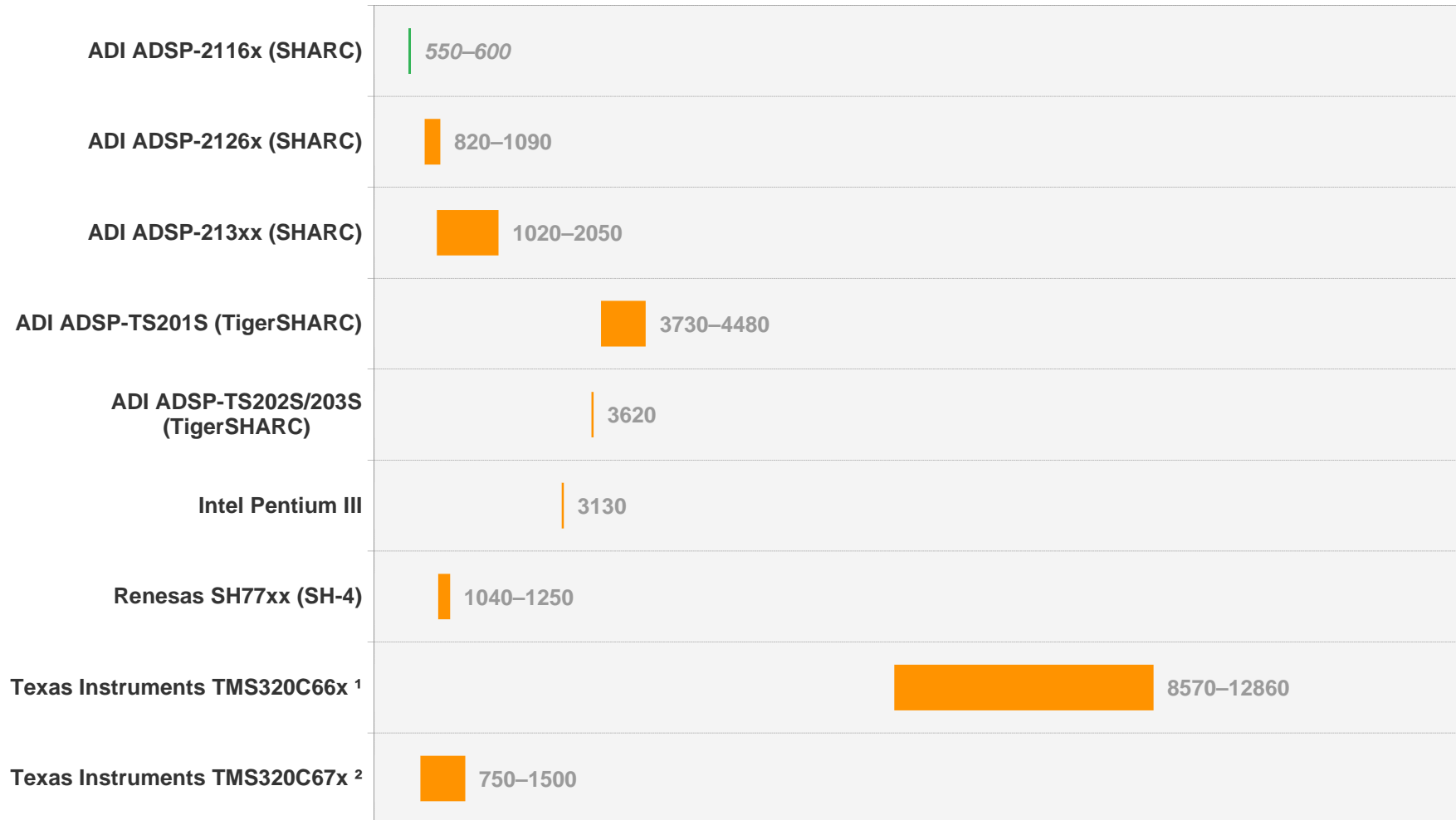
Speed Scores for Floating-Point Packaged Processors

Updated February 2011

Copyright © 2011 Berkeley Design Technology, Inc.

No reproduction or reuse is permitted without the express authorization of BDTI.

See page 2 for details.



¹ Score for one core

² Score does not apply to TMS320C67x+ parts (e.g., the TMS320C672x)

All processors benchmarked with 32-bit floating-point data.

BDTIsimMark2000™ scores may be based on projected clock speeds. For information, see www.BDTI.com/Services/Benchmarks



Speed Scores for Floating-Point Packaged Processors

Updated February 2011

Copyright © 2011 Berkeley Design Technology, Inc.

No reproduction or reuse is permitted without the express authorization of BDTI.



Processor Family	Clock Rate (min-max)	BDTImark2000™, BDTIsimMark2000™ (min-max)
ADI ADSP-2116x (SHARC)	100–110 MHz	550–600
ADI ADSP-2126x (SHARC)	150–200 MHz	820–1090
ADI ADSP-213xx (SHARC)	200–400 MHz	1020–2050
ADI ADSP-TS201S (TigerSHARC)	500–600 MHz	3730–4480
ADI ADSP-TS202S/203S (TigerSHARC)	500 MHz	3620
Intel Pentium III	1400 MHz	3130
Renesas SH77xx (SH-4)	333–400 MHz	1040–1250
Texas Instruments TMS320C66x ¹	1000–1500 MHz	8570–12860
Texas Instruments TMS320C67x ²	150–300 MHz	750–1500

¹ Score for one core

² Score does not apply to TMS320C67x+ parts (e.g., the TMS320C672x)

All processors benchmarked with 32-bit floating-point data.

BDTImark2000™, BDTIsimMark2000™: The BDTImark2000™ and BDTIsimMark2000™ provide a summary measure of signal processing speed. BDTIsimMark2000™ scores may be based on projected clock speeds. For more info and scores see www.BDTI.com/Services/Benchmarks.